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... that supplies a virtual floppy image and redirects INT 13. ... | 0 | 1 | COMMAND | Net

Bios command code. High order bit |. | Hex| | | set indicates no-wait mode. ...

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... sounds a little strange to you, just wait until you ... or 8 bits are available in the

INT 13 interface ... The SCSI host adapter BIOS converts this "fake" CHS address ...

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... possible damage to the system board, wait five seconds ... Diskette

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[PS] Final Year Project An IBM PC Emulator

D Hedley - smaug.fh-regensburg.de

... bios.c ... For example, the keyboard controller and interrupt controller wait for certain

inputs on their various pins, and then they might signal an Interrupt ...

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G Blanchet - tsi,enst.fr

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... Charli, JL, Cruz, C., Vargas, MA, and Joseph-Bravo, P. (1988) Neurochem. Int. 13,

237-242. 13. ... 239-248, BIOS Scientific Publishers Limited, Oxford, U. K. 14. ..

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of various origins. ... 23 Page 7. Int 13 h Int25 h Int26 h These ...

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from now. BIOS Int 13 - the 8.5 GB limit. At most 1024 ...

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... 直接修改INT 13的处理程序,使其具有硬盘分区写保护的功能。

因为不同的机型的BIOS代码差别较大 ...

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硬盘锁的研究与应用 - group of 2 » 第小梅 - 南京农专学报, 1998 - 维普资讯 ... 就是要拦截住所有对 C 盘的写操作。由于不论采用什么方式写硬盘,

都要通过BIOS的INT 13 H中断调用 ...

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[воок] Imagination and Chance: The Difference Between the Thought of Ricoeur and Derrida

L Lawlor - 1993 - books.google.com

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R Stata M Abadi

B Serpette

K SIKORSKI, F STENGER, J SCHWING - ACM Transactions on Mathematical Software, 1984 - portal acm.org ... Therefore we first compute MACHEP and then change the input ... C THIS SUBROUTINE COMPUTES

INTEGRAL OF FUNCTIONS WHICH ... INT 127% INT 1280 INT 129@ INT 13%% INT 131 ...

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... And the operational step describes how instructions change the internal state of

the ... v) possibility of multiple entries for a subroutine (vi) continuation ...

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of multilevel returns, which are discussed below. ... call subroutine 7 return ...

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[PS] User's Guide to a Boundary Element Package for Solving Integral Equations on Piecewise Smooth ... - group of 8 » KE Atkinson - Reports on Computational Mathematics, 1998 - math.uiowa.edu

... 1.1.1 The structure of the triangulation process The triangulation is initiated

with a call to a subroutine INIT which is to be supplied by the user. ...

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colleague^^^_ based on the CHEMKIN" and transport subroutine libraries.26 ...

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Specification - group of 12 »
D Version - 3 - emc.com.tw

... 8 level stacks for subroutine nesting ... This specification is subject to change without

further ... I/O pin ■ Default value at power-on reset INT 13 I ■ External ...

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M Noro, N Takayama - math.sci.kobe-u.ac.jp

... November 20, 1997 | November 17, 2000, August 27, 2001, January 20, 2002 (minor

change), SeechangeLog about changes after ... 2. Linking as a subroutine library. ...

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Optimal control of peritoneal dialysis - group of 2 » P Zunino, D Mastalli - pos.sissa.it

... mass conservation principle, we end up with a system of M = 2m+2 ordinary differential

equa-tions that describe the rate of change of the unknowns V ...

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A Erdemir, SJ Piazza - Journal of Biomechanical Engineering, 2004 - link,aip.org

... Schittkowski, K., 1986, "NLPQL: A Fortran Subroutine Solving Constrained Nonlinear ...

Clinical Results and Biomechanical Evaluation," Foot Ankle Int., 13, pp. ...

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... r o (27r)-i[1 +exp{int(13»- 1 + I)}]-' J - e" 14 and so the first term in (1) is

negligible. ... This latter integral was evaluated using a standard subroutine. ...

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... JVML has a subroutine construct which is used for the compilation of Java's try-

finally statement. ... Inside a subroutine, the type 4 Page 13. ...

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B White

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J Lepreau

... Overview PXE (Preboot Execution Environment) is an existing open industry specification

L Stoller

for enterprise network clients to automatically download software ... Cited by 3 - Related Articles - View as HTML - Web Search

R Ricci

S Guruprasad

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... and events can be reproduced in an emulated or wide-area environment. ... conditionals, and arbitrary functions to drive experiment configuration and execution. ...

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[CITATION] Preboot Execution Environment (PXE) Specification

PXE Intel - Intel Corporation, 1999

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Disk cloning program 'dolly+'for system management of pc linux cluster

A Manabe - Computing in High Energy Physics and Nuclear Physics, 2001 - corvus.kek.jp

. Since Dolly+ is just an application running on Linux, to start it on the nodes,

PXE(Preboot eXecution Environment)[2] and kickstart are required. ...

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Automatic configuration and execution of Internet experiments on an actual node-based testbed - group of 3 »

T Miyachi, K Chinen, Y Shinoda - Testbeds and Research Infrastructures for the Development of ..., 2005 - ieeexplore.ieee.org

... This section describes the design of the system which supports the experiment

execution in the environment de- scribed until now. ...

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Enabling preOS desktop management - group of 3 »

T Cruz, P Simoes - Integrated Network Management, 2003. IFIP/IEEE Eighth ..., 2003 - ieeexplore.ieee.org

... to typical desktop management solutions whose pre-boot capabilities are non ... firmware

and standard extensions, including Preboot Execution Environment Boot ROM (P.

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Rethinking Desktop Management

T Cruz, P Simões - Proceedings of APNOMS, 2002 - eden.dei.uc.pt

... The OpenDMS approach for pre-boot management relies in a ... extension, commonly known

as Preboot eXecution Environment (PXE ... which OS and which environment will be ...

Cited by 1 - Related Articles - View as HTML - Web Search

[CITATION] Preboot Execution Environment (PXE) Specification

M Henry, M Johnston - 1999 - September

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Network Working Group R. Droms Internet-Draft Cisco Systems Expires: October 10, 2003 April 11, 2003

UDO Codes - tools.ietf.org

... The following option codes are used in the "Preboot Execution Environment (PXE)

Specification, Version 2.1" [4]. However, none of these option codes have been ...

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D Inc - csm.oml.gov

... nodes in serial to facilitate pre-boot execution environment (PXE) [2 ... WfM incorporates

the following specifications: Preboot eXecution Environment (PXE), Wake ...

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... By trapping the BIOS disk interrupt, a virus can intercept all BIOS disk activity, including reads of boot sectors, or disguise disk accesses to infect as part ... Cited by 30 - Related Articles - View as HTML - Web Search

[DOC] FlightLinux Project

E Testbed, PH Stakem - flightlinux.gsfc.nasa.gov

... 00055 *. 00056 * Do a disk interrupt. 00057 *. 00059 *. ... 00072 * Recover our DS segment. 00075 We can now use the bios data table again *****. ...

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P Descriptions - hep.physics.lsa.umich.edu

.. Signal Pin Type DIRQ1 75 I Function Disk Interrupt is an input to the PCI0646 used to generate the IRQ14 output when the primary IDE channel is in legacy mode. \dots Related Articles - View as HTML - Web Search

ManyDisk Professional Virtual Hard Disk Manager

K Chen - innovexpo.itee.uq.edu.au

... The first limit is the 504MB limit. This is due to old PC BIOS only allowing ... The second limit is due to the disk interrupt INT 13h and the FAT partition ... Related Articles - View as HTML - Web Search

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W Milwaukee, C Weedall - people.msoe.edu

... when working with the transfer from or to a disk. Interrupt-Driven I/O ... The adapter's memory is mapped into system address space through the PCI BIOS, a ... Related Articles - View as HTML - Web Search

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GL Landsberg - Physics-Uspekhi, 1991 - turpion.org

... On IBM PC com- puters they are a part of BIOS (Basic Input-Output Sys- tem). There are also software interrupts which are support- ed by any operating system. ... Web Search

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PDV Web - marthel.pl

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SASK IR - smsc.com

... AMI Keyboard BIOS ROM 128 Pin QFP, 3.2mm footprint Package Page 2. SMSC DS - LPC47M192 ... PART# PACKAGE KEYBOARD BIOS LPC47M192-NC 128 Pin QFP AMI Page 4. ...

100 Pin Enhanced Super I/O for LPC Bus with SMBus Controller for Commercial Applications

FD Controller, MMP Port, T UART - smsc.com

... The "x" in the part number is a designator that changes depending upon the particular BIOS used inside the specific chip. "2 ...

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F Controller M Port

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K Chen - innovexpo.itee.uq.edu.au ... s 5%-inch bay that enables end users to boot from a $\,$... This is due to old PC BIOS only

allowing ... The second limit is due to the disk interrupt INT 13h and the FAT ...

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E Testbed, PH Stakem - flightlinux.gsfc.nasa.gov

... Id86 -0 -s -o boot/setup -M boot/setup.o > Setup.map. ... 00055 *. 00056 * Do a disk

interrupt. 00057 *. ... 00075 We can now use the bios data table again *****

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